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PTO/SB/08A (08-00)

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete if Known</b>	
			Application Number	
			Filing Date	
			First Named Inventor	MAAMARI, Fadi
			Group Art Unit	
			Examiner Name	
Sheet 1 of 2	Attorney Docket Number		LVPAT028US	

[illegible][illegible]

Examiner Signature	<i>Davidson</i>	Date Considered	5/24/04
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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<b>Substitute for form 1449B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number			
		Filing Date			
		First Named Inventor	MAAMARI, Fadi		
		Group Art Unit			
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	LVPAT028US

<b>OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DT		FUJIWARA et al., "On the Acceleration of Test Generation Algorithms", IEEE Transactions on Computers, Vol. C-32, No. 12, December, 1983	
DT		KOSEKO et al., "Tri-state Bus Conflict Method for ATPG Using BDD, 1993, IEEE	
DT		ROTH et al., "Programmed Algorithms to Compute Tests to Detect and Distinguish Between Failures in Logic Circuits", IEEE Transactions On Electronic Computers, October 1967	

Examiner Signature	<i>Davidson</i>	Date Considered	5/24/04
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**Complete if Known**

<b>Application Number</b>	09/807,299
<b>Filing Date</b>	03/26/2001
<b>First Named Inventor</b>	MAAMARI, Fadi
<b>Group Art Unit</b>	2881
<b>Examiner Name</b>	N / A
<b>Attorney Docket Number</b>	LVPAT028US

Sheet 1 of 1

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5/24/04

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